

INTEGRATED SILICON SOLUTION, INC.

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Typed or Printed Name Lou Yang							
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NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.							
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STATEMENT UNDER 37 CFR 3.73(b)						
Applicant/Patent Owner: Integrated Silicon Solution, Inc.						
Application No./Patent No.: 6,917,934 Filed/Issue Date: issued 07/12/200	5					
Entitled: Segmented Bus Architecture For Improving Speed In Integrated Circuit Memories	· ·					
Integrated Silicon Solution, Inc. , a Corporation (Name of Assignee) (Type of Assignee, e.g., corporation, par	ntnership, university, government agency, etc.)					
states that it is: 1. the assignee of the entire right, title, and interest; or						
an assignee of less than the entire right, title and interest (The extent (by percentage) of its ownership interest is%)						
in the patent application/patent identified above by virtue of either:						
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[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) me Division in accordance with 37 CFR Part 3, to record the assignment in the reco 302.08]	rds of the USPTO. See MPEP					
The undersigned (whose title is supplied below) is authorized to act on behalf of the assig	inee. 8/14/08					
Signature	Date					
Lou Yang	408-969-5124					
Printed or Typed Name	Telephone Number					
V.P. of Business Development Title						

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